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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/619,073	07/14/2003	Stefan Eder	KRWE-0001-UT1	6970
22506	7590	05/01/2007	EXAMINER	
JAGTIANI + GUTTAG			BAROT, BHARAT	
10363-A DEMOCRACY LANE			ART UNIT	PAPER NUMBER
FAIRFAX, VA 22030			2155	
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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/619,073	EDER ET AL.
	Examiner Bharat N. Barot	Art Unit 2155

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 14 July 2003.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 15-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 15-28 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 14 July 2003 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____.
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date <u>7/14/03 & 2/22/06</u> .	5) <input type="checkbox"/> Notice of Informal Patent Application
	6) <input type="checkbox"/> Other: _____.

DETAILED ACTION

Drawings

1. This application has been filed with informal drawings, which are acceptable for examination purposes only. Formal drawings will be required when the application is allowed.

Claim Objections

2. Claims 18-19 are objected to because of the following informality:

Claims 18-19 line 1 recited "characterized in that" which should be --**wherein**--.

Appropriate corrections are required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 15-28 are rejected under 35 U.S.C. 102(e) as being anticipated by Tattari (U.S. Patent No. 6,145,095). Tattari's patent meets all the limitations for claims 15-28 recited in the claimed invention.

5. As to claim 15, Tattari teaches a method for setting up a program-controlled circuit arrangement (figures 2-3; column 1 lines 8-13; and column 2 lines 14-67) with a processor unit (figure 1 reference number 24), an assigned non-volatile start procedure memory (figure 1 reference number 22), and an interface (figure 1 reference number 21) for connection to a data transmission network (figure 1 reference number 1), the processor unit is set up in such a way that, after being switched on, it executes a start procedure stored in the start procedure memory, a start procedure (initial load program) is stored in the start procedure memory, the start procedure is set up in such a way that during its execution the processor unit connects up by the interface to an operating program server (figure 1 reference number 10) and from this loads operating program instructions into a main memory (figure 1 reference number 23) assigned to the processor unit (figures 1 and 4; column 1 line 54 to column 2 line 13; and column 6 line 55 to column 7 line 15).

6. As to claim 16, Tattari teaches that the start procedure is set up in such a way that during its execution the processor unit loads and executes a download procedure (advance load and application program) from a non-volatile download procedure memory into the main memory, and the download procedure is set up in such a way that during its execution the processor unit connects up by the interface to the operating program server and from this loads operating program instructions into the main memory assigned to the processor unit (figures 1 and 4; column 1 line 54 to column 2 line 13; and column 6 line 55 to column 7 line 15).

7. As to claims 17-18, Tattari teaches that the circuit arrangement has an exchange arrangement for exchanging data packets within the data transmission network; and the circuit arrangement is a circuit for providing a telephone and/or fax service via the data transmission network (figures 1 and 4; column 1 lines 8-13; column 1 line 54 to column 2 line 13; and column 6 line 55 to column 7 line 15).

8. As to claims 19-20, it is inherent in the art at the time the invention was made to integrated a memory with a processor unit on a semiconductor module.

Tattari teaches that the start procedure memory is integrated with the processor unit on a semiconductor module (figure 1 reference numbers 22 and 24); and the download procedure memory and the processor unit are integrated in various semiconductor modules (figure 1 reference numbers 10 and 20) and the download procedure is loaded serially from the download procedure memory (figures 1 and 4; column 1 line 54 to column 2 line 13; and column 6 line 55 to column 7 line 15).

9. As to claim 21, Tattari teaches that the download procedure memory is a serial EEPROM (FLASH memory) (column 7 lines 16-25).

10. As to claims 22-28, they are also rejected for the same reasons set forth to rejecting claims 15-21 above, since claims 22-28 are merely an apparatus for the method of operation defined in the method claims 15-21.

Additional References

11. The examiner as of general interest cites the following references.

- a. Bengtsson et al, U.S. Patent No. 6,356,942.
- b. Toyoda, U.S. Patent No. 6,335,966.
- b. Takida, U.S. Patent No. 5,305,457.

Contact Information

12. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Bharat Barot** whose Telephone Number is **(571) 272-3979**. The examiner can normally be reached on Monday-Friday from 9:30 AM to 6:00 PM. Most facsimile-transmitted patent application related correspondence is required to be sent to the Central FAX Number **(571) 273-8300**.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Saleh Najjar**, can be reached at **(571) 272-4006**.

Patent Examiner Bharat Barot

Art Unit 2155

April 19, 2007

Bharat Barot
BHARAT BAROT
PRIMARY EXAMINER